



KALASALINGAM

ACADEMY OF RESEARCH & EDUCATION

(DEEMED TO BE UNIVERSITY)



Under sec. 3 of UGC Act 1956. Accredited by NAAC with "A" Grade

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Department of Electronics and Communication Engineering

Detail of Funded Projects – Ongoing / Completed

S.No	Name of the Scheme/Project/Endowments/ Chairs	Name of the Principal Investigator/ Co Investigator (if applicable)	Name of the Funding agency	Type (Government/ Non-Government)	AY	Date	Year of Award	Funds provided (INR in lakhs)	Durati on of the project	Status	Lab Utilized	Research Domain/ centre
1	Toycathon 2021	Dr.J.Deny	AICTE-MIC	Government	2021-2022	29-07-2021	2021	200000	NA	Ongoing	Institutional	Institutional
2	Wearable / Portable electrical muscle stimulation belt for cervicalgia patients	Dr.J.Deny	EDII, TN	Government	2021-2022	01-07-2021	2021	200000	2	Ongoing	DSP Lab	Communication and Networking
3	AICTE-ATAL FDP on Future Perspective of Semiconductor devices	Dr.J.Charles Pravin	AICTE	Government	2020-2021	29-06-2021	2021	93000	NA	Completed	VLSI Research Lab	VLSI Design and Embedded System Lab
4.	Remote Tasking of Sanitization and Health Assurity for mitigating risk in the wake of the pandemic	Mr.R.Radeep Krishna	IEEE, HAC	Non-Government	2020-2021	20-06-2021	2021	36469	1	Ongoing	MPMC Lab	VLSI Design and Embedded System Lab
5	Scheme for Promoting Interests, Creativity & Ethics among Students' (SPICES) Scheme	Dr.P.Sivakumar	AICTE	Government	2020-2021	22-04-2021	2021	100000	1	Ongoing	Institutional	Institutional

6	Non-Invasive Sonometer along with clinically practiced dexta images for estimating bone mineral density for osteoporosis detection in elderly women	Dr.M.Pallikonda Rajasekaran, Dr.G.Vishnuvarthan Dr.V.Munee swaran	DST	Government	2020-2021	24-03-2021	2021	2840200	2	Ongoing	DSPSD Lab	Image Processing Research Lab
7	Optimum Energy Solar System (IVP)	Dr.J.Deny	EDII-Tamilnadu	Government	2020-2021	4/11/2020	2020	224000	1	Ongoing	MPMC Lab	VLSI Design and Embedded System Lab
8	MSME-TBI	Dr. J. Deny	MSME	Government	2019-2020	2020	2020	25000000	5 Years	Ongoing	Institutional	Institutional
9	Proposal for Embedded System and Robotics Lab	Dr.P.Sivakumar	MODROB	Government	2019-2020	(Approved on 18-12-2019)	2020	1959830		Ongoing	MPMC Lab	VLSI Design and Embedded System Lab
10	Cyber Physical systems: Design Techniques and Applications SDTP	Dr.P.Sivakumar	MODROB	Government	2019-2020	(Approved on 18-12-2019)	2020	400000	3 Months	Completed	VLSI Research Lab	VLSI Design and Embedded System Lab
11	Organization of STEM Exhibition & Activity Corners for Rural Students of Virudhunagar District	Dr.P.Sivakumar	DST-STEMM	Government	2019-2020	(25-11-2019)	2020	1475000	3 Months	Ongoing	Institutional	Institutional
12	Study effectiveness of Technology Business Incubators in South India	Dr.J.Deny	NSTMIS	Government	2019-2020	(9-10-2019)	2019	700000	3	Ongoing	Institutional	Institutional
13	DST NIMAT Project for Entrepreneur Awareness Camp (EAC)/Technology based Entrepreneurship Development Programme (TEDP)	Dr.J.Deny	National Implementing and Monitoring Agency for Training (NIMAT) under the aegis of NSTEDB, Department of Science & Technology, GoI	Government	2019-2020	(12-08-2019)	2019	380000	1	Completed	Institutional	Institutional
14	3D printed flexible optical fibers using biodegradable PHBV/Graphene Nanocomposites for Biophotonics applications	Dr.M.Pallikonda Rajasekaran	DST	Government	2018-2019	(19-02-2019)	2019	780000	3	Ongoing	VLSI Research Lab	VLSI Design and Embedded System Lab

15	An IoT based Smart Artificial Pancreas and an ICT Platform for Treating Diabetes in Elderly	Dr. Seshadhri Srinivasan, Dr. B. Subathra, Dr. Kim	DST-SEED	Government	2018-2019	2018-2021	2018	5640886	3 Years	Ongoing	MPMC Lab	VLSI Design and Embedded System Lab
16	Development of Electronics Lockers with multiple keys using Visual Cryptography Scheme	Mr.Harish R, Dr.Suthendran	DST-EDII	Government	2018-2019	(14-09-2018)	2018	100000	1	Completed	DSP Lab	Communication and Networking
17	Smart Tube Light	Dr.J.Deny	DST-EDII	Government	2018-2019	(14-09-2018)	2018	100000	1	Completed	LDIC Lab	VLSI Design and Embedded System Lab
18	DST NIMAT Project for Conducting Entrepreneurship Awareness Camp	Dr.J.Deny	National Implementing and Monitoring Agency for Training (NIMAT) under the aegis of NSTEDB, Department of Science & Technology, GoI	Government	2018-2019	(03-07-2018)	2018	100000		Completed	Institutional	Institutional
19	Smart Navigation system for Visually challenged Person	Mr.P.Manikandan	TNSCST-DoTE	Government	2017-2018	(27-01-2018)	2018	8000	1	Completed	Communication Lab	Communication and Networking
20	Design and implementation of low cost veterinary physiological monitor	Dr. J. D Mr. Sakthivel	DST-IEDC	Government	2017-2018	(21-11-2017)	2017	100000	1	Completed	LDIC Lab	VLSI Design and Embedded System Lab
21	Smart Hat for visually challenged person	Mr.P.Manikandan	DST-EDII	Government	2017-2018	(21-11-2017)	2017	100000	1	Completed	Communication Lab	Communication and Networking
22	DST NIMAT Project for Conducting EAC/FDP	Dr.J.Deny	National Implementing and Monitoring Agency for Training (NIMAT) under the aegis of NSTEDB, Department of Science & Technology, GoI	Government	2017-2018	(11-07-2017)	2017	195000	1	Completed	Institutional	Institutional

23	Smart Cart for super market	Mr.Vengat Rahul	DST-Start Up Nidhi	Government	2016-2017	(16-06-2017)	2017	1000000	1	Completed	LDIC Lab	VLSI Design and Embedded System Lab
24	Design and Development of Low cost photomograph for identification of thyroid dysfunction	Dr.M.Pallikonda Rajasekaran	DST-Start Up Nidhi	Government	2016-2017	(16-06-2017)	2017	1000000	1	Completed	DSP Lab	Communication and Networking
25	Coin Based Mobile Charger	Dr.B.Perumal	DST-EDII	Government	2016-2017	(12-09-2016)	2016	100000	1	Completed	LDIC Lab	VLSI Design and Embedded System Lab
26	Constructing the Hindi Spoken Language Database	Dr.Kyung Tae Kim	Human Media Co. (Korea)	Non-Government	2015-2016	(30-03-2016)	2016	1328742	7 months	Completed	DSPSD Lab	Image Processing Research Lab
27	Implementation of Highly Automated UAV with GPS for product delivery	Mrs.K.S.Dhanalakshmi	DST-EDII	Government	2015-2016	(04-09-2015)	2015	100000	1	Completed	MW lab	Communication and Networking
28	Center for Signal Processing and VLSI Design	Dr.M.Pallikonda Rajasekaran/HOD ECE	DST-FIST	Government	2013-2014	2013	2013	4200000	5	Completed	DSPSD Lab and VLSI Research Lab	Image Processing Research Lab and VLSI Design and Embedded System Lab
29	Development of Hybrid Metaheuristic Algorithms for Solving Power System Operations Planning Under Deregulated Environment	Dr.S.Kannan [Professor,ECE]	DST	Government	2008-2011	9 th May 2008	2008	990720	3	Completed	DSP Lab	Communication and Networking
30	Combinatorial Optimization in Packing Problems specific to VLSI Design	Dr.P.Subburaj, Dr.S.Saravanasakar	DST	Government	2007-2010	30 th May 2007	2007	994000	3	Completed	VLSI Lab	VLSI Design and Embedded System Lab